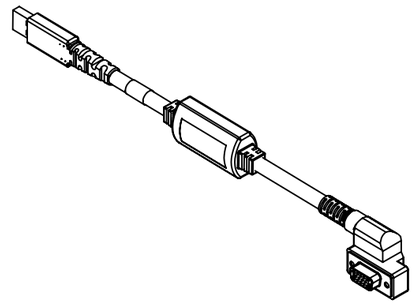
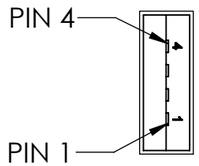
TABLE 1: WIRE RUN LIST					
P1	USB SIGNAL	WIRE COLOR	SERIAL SIGNAL	WIRE COLOR	P2
1	VCC	RED	GND	GREEN	3
2	D-	WHITE	RXD	RED	14
3	D+	GREEN	TXD	ORANGE	15
4	GND	BLACK			
SHELL	SHELL	SHIELD	CHASSIS GND	SHIELD	SHELL

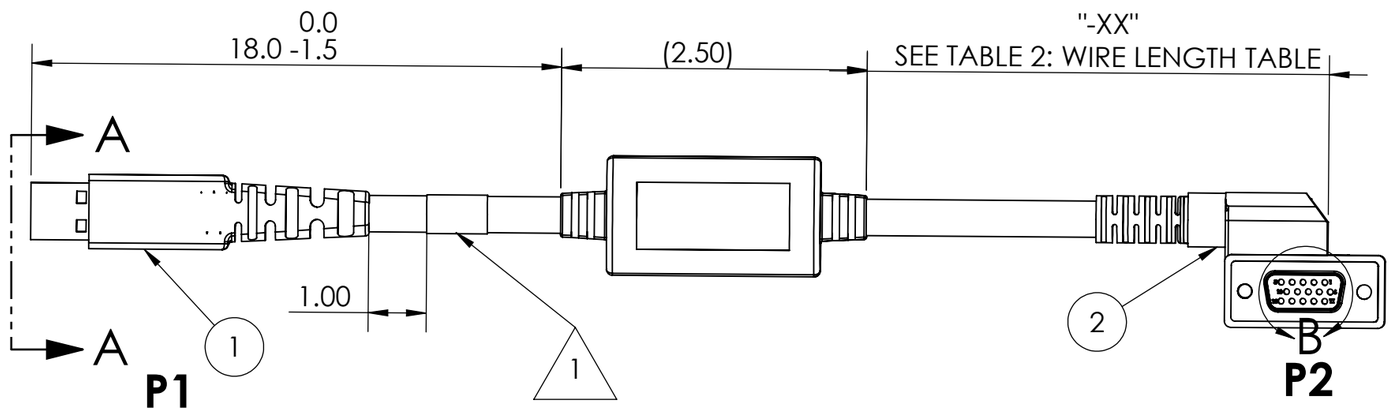
TABLE 2: WIRE LENGTH TABLE		
P/N "-XX"	LENGTH	TOLERANCE
-	3.0	+1.0
03	15.5	+2.0
05	39.5	+3.0
07	63.5	+4.0

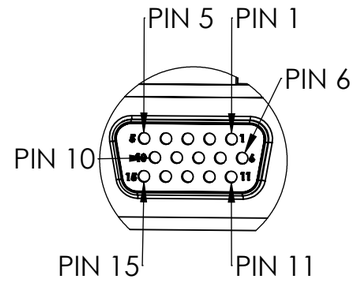
REVISIONS			
REV.	DESCRIPTION	DATE	ENG
A	INITIAL RELEASE	06/04/2026	GMW
B	CREATED FAMILYD DRAWING	09/16/2026	GMW





P1 PIN LAYOUT
(MATING FACE)
SECTION A-A
SCALE 1:1.25





P2 PIN LAYOUT
(MATING FACE)
SECTION B
SCALE 2:1.25

NOTES:

1. WIRING SHALL BE DONE IN ACCORDANCE WITH TABLE 1 WIRE RUN LIST.

2. MARKING REQUIREMENTS:
   - A. LABEL(S) SHALL BE MARKED IN ACCORDANCE WITH MIL-STD-130 APPLICABLE.
   - B. LABEL(S) SHALL BE WHITE WITH BLACK TEXT.
   - C. LABEL(S) SHALL BE PLACED WHERE SHOWN IN DRAWING.
   - D. ADHESIVE WRAP AROUND LABEL(S) TO BE USED.
   - E. LABEL(S) SHALL BE MARKED WITH THE BELOW INFORMATION ONLY. REV MARKING PER PO.
     (YYYY = YEAR OF MFG, MM = MONTH OF MFG, DD = DAY OF MFG, XXXXX = PO NUMBER)
     PN: SN-CY014-XX REV. X
     CAGE: 1HJX9
     LOT: TYYYYMMDDPXXXXX

3. PACKAGING REQUIREMENTS:
   - A. EACH COMPLETED ASSEMBLY SHALL BE PACKAGED IN A ZIP POLYBAG.
   - B. CONNECTOR(S) SHALL BE PACKAGED TO PREVENT DAMAGE DURING SHIPPING. BUBBLE WRAP OR PROTECTIVE COVER MUST BE USED.
   - C. POLYBAG SHALL BE MARKED WITH SUPPLYNET LOGO, PRODUCT QR CODE, PART NUMBER, CAGE CODE, AND LOT NO, PER SN-STD-LBL REQUIREMENTS.

4. WORKMANSHIP REQUIREMENTS:
   - A. MANUFACTURE TO IAW/WHMA A-620 CLASS 2 REQUIREMENTS.
   - B. SOLDERING SHALL BE COMPLIANT WITH IAW J-STD-001 AS APPLICABLE.
   - C. CONNECTOR(S) AND WIRE SHALL BE FREE FROM ANY SCRATCHES, CHIPS, AND DEFECTS.

5. INSPECTION REQUIREMENTS:
   - A. EACH COMPLETED ASSEMBLY TO BE 100% TESTED FOR SHORTS, CONTINUITY, AND JACKET RESISTANCE (250V @100MOHM MIN.)
   - B. INSPECT TO IPC/WHMA A-620 CLASS 2 REQUIREMENTS.

BILL OF MATERIALS					
ITEM NO.	PART NUMBER	QTY.	UOM	DESCRIPTION	REV.
1	RSPT2501-06	1	EA	SERIAL TO USB CABLE, 6 FEET LENGTH, UNTERMINATED FTDI CHIPSET NO SLEEP MODE	N/A
2	09HD15	1	EA	HD15 FEMALE CONNECTOR	N/A

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES (IN)
TOLERANCES:
1 PLACE ±.1 2 PLACES ±.03 3 PLACES ±.005
ANGLES 1 PLACE ±.1° 2 PLACES ±.5°
BREAK EDGES .005 - .020
FILLETS .005 - .020

GEOMETRIC TOLERANCING PER:
ASME Y14.5 -2018

FINISH: SEE DRAWING NOTES

MATERIAL: SEE DRAWING NOTES

THIRD ANGLE PROJECTION

	NAME	DATE
DRAWN	GMW	06/02/2026
CHECKED	MOB	06/03/2026

PROPRIETARY AND CONFIDENTIAL

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DO NOT SCALE DRAWING

SupplyNet DESIGN ENGINEERING MANUFACTURING

TITLE:
DAGR J2 SERIAL TO USB PROGRAMMING CABLE

SIZE	CAGE CODE	DWG. NO.	REV
B	1HJX9	SN-CY014-XX	B

SCALE: N/A | REF. NUMBER: SN1541 | SHEET 1 OF 1